

## SYC Course Descriptions for CYAA races with a windward gate

**COURSE 1** Leave all marks to starboard. 4.4nm

Start — 1 — 2 — 3 — Finish

**COURSE 2** Leave all marks to port. 4.4nm

Start — 3 — 2 — 1 - Finish

**COURSE 3** Leave all marks to starboard. 6.3nm

Start — 5 — 2 — 3 — Finish

**COURSE 4** Leave all marks to port. 6.5nm

Start — 3 — 2 — 5 — Finish

**COURSE 5** Leave all marks to starboard. 4.1nm

Start — 1 — 4(pass) - 7 - Finish

**COURSE 6** Leave all marks to starboard. 7.3nm

Start — 5 — 4 (pass) — 7 — Finish

**COURSE 7** Leave all marks to starboard. 7.9nm

Start — 1 — 2 — 3 — 4 — 1 — 4 (pass) — 3 - Finish

**COURSE 8** Leave all marks to starboard. 8.2nm

Start — 1 — 4(pass) — 7 — 4 - 1 - 4(pass) — 7 - Finish

**COURSE 9** Leave all marks to starboard. 8.2nm

Start — 1 — 2 — 7 — 4 — 1 — 4 (pass) — 3 - Finish

**COURSE 10** Leave all marks to starboard. 8.5nm

Start — 1 — R2 — 3 - Finish

**COURSE 11** Leave all marks to starboard. 8.7nm

Start — 1 — 6 - 3 — 4 — 2 — 3 - Finish

**COURSE 12** Leave all marks to starboard. 9.8nm

Start — 5 — 2 — 3 — 4 — 1 — 4 (pass) — 3 — Finish

**COURSE 13** Leave all marks to starboard. 9.8nm

Start — 5 — 4(pass) — 7 — 4 - 1 - 4(pass) — 3 - Finish

**COURSE 14** Leave all marks to starboard. 9.9nm

Start — 5 — 4(pass) — 3 — 4 - 1 - 4(pass) — 7 - Finish

**COURSE 15** Leave all marks to port. 4.1nm

Start — 7 — 4 (pass) — 1 — Finish

**COURSE 16** Leave all marks to port. 7.3nm

Start — 7 — 4 (pass) — 5 — Finish

**COURSE 17** Leave all marks to port. 7.6nm

Start — 7 — 4(pass) — 1 — 4 - 3 - 4(pass) — 1 - Finish

**COURSE 18** Leave all marks to port. 7.9nm

Start — 3 — 2 — 1 - 4 — 3 — 4 (pass) — 1 — Finish

**COURSE 19** Leave all marks to port. 8.2nm

Start — 7 — 2 — 1 - 4 — 3 — 4 (pass) — 1 — Finish

**COURSE 20** Leave all marks to port unless otherwise stated. 8.8nm

Start - 7- 6 - 1 - 4 - 2 (Stbd) - 3 (stbd) - Finish

**COURSE 21** Leave all marks to port. 9.1nm

Start — 3 — 6 - 1 - 4 — 2 — 1 - Finish

**COURSE 22** Leave all marks to port unless otherwise stated. 9.7nm

Start - 3 - 6 - 5 - 2(Stbd) - 1 - Finish

**COURSE 23** Leave all marks to port. 6.3nm

Start — 3 — 2 — 5 — Finish

**COURSE 24** Leave all marks to port. 9.8nm

Start — 7 — 4(pass) — 5 — 4 - 3 - 4(pass) - 1 - Finish

**COURSE 25** Leave all marks to port. 9.9nm

Start — 3 — 4(pass) — 5 — 4 - 7 - 4(pass) — 1 - Finish

**COURSE 26** Leave all marks to starboard. 5.7nm

Start — 1 — 6 - 7 — Finish

**COURSE 27** Leave all marks to starboard. 6.6nm

Start — 5 — 2 — 7 — Finish

**COURSE 28** Leave all marks to starboard. 7.2nm

Start — 5 — 6 - 3 — Finish

**COURSE 29** Leave all marks to Starboard. 8.2nm

Start — 1 — 4(pass) — 7 — 4 - 1 - 4(pass) — 7 - Finish

**COURSE 30** Leave all marks to starboard. 6.3nm

Start — 5 — 4 (pass) — 7 — Finish

**COURSE 31** Leave all marks to starboard. 5.6nm

Start — 1 — 6 - 3 — Finish

**COURSE 32** Leave all marks to port unless otherwise stated. 5.7nm

Start — 7 — 6 - 1 - Finish

**COURSE 33** Leave all marks to port. 10.7nm

Start — 7 — 2 — 5 — 4 — 7 — 4 (pass) — 1 — Finish

**COURSE 34** Leave all marks to port. 8.2nm

Start — 7 — 4(pass) - 1 - 4 - 7 — 4(pass) - 1 - Finish

**COURSE 35** Leave all marks to port. 7.2nm

Start — 3 — 6 - 5 — Finish

**COURSE 36** Leave all marks to port. 5.6nm

Start — 3 — 6 - 1 - Finish

**COURSE 37** Leave all marks to starboard. 7.3nm

Start — 5 — 6 - 7 — Finish

**COURSE 38** - Leave all marks to port. 7.3nm

Start — 7 — 6 - 5 — Finish